



RESET ALL

PATCH^{vt}

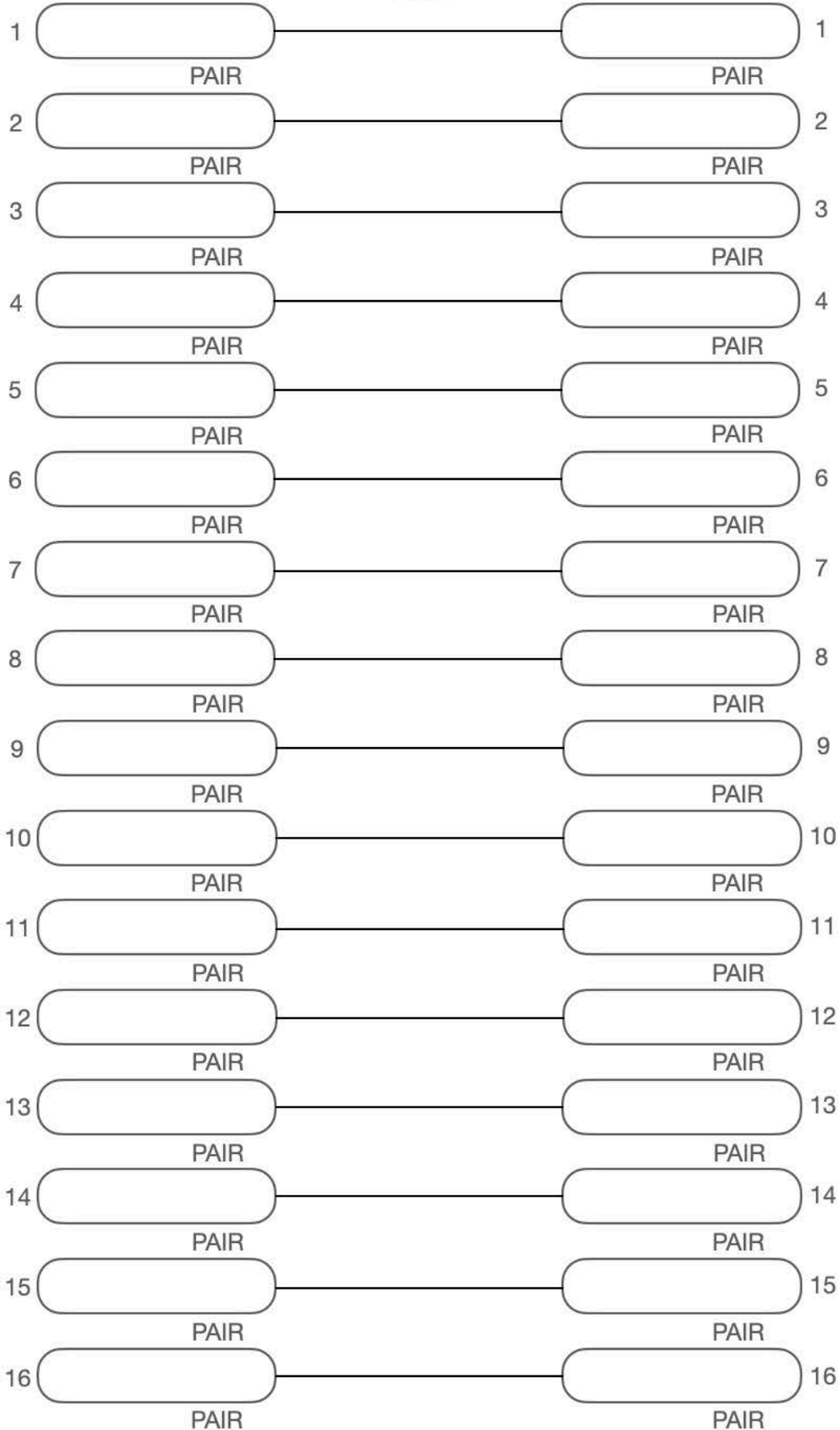
HARDWARE SETUP TEMPLATE

48V

INPUTS

LINK

OUTPUTS

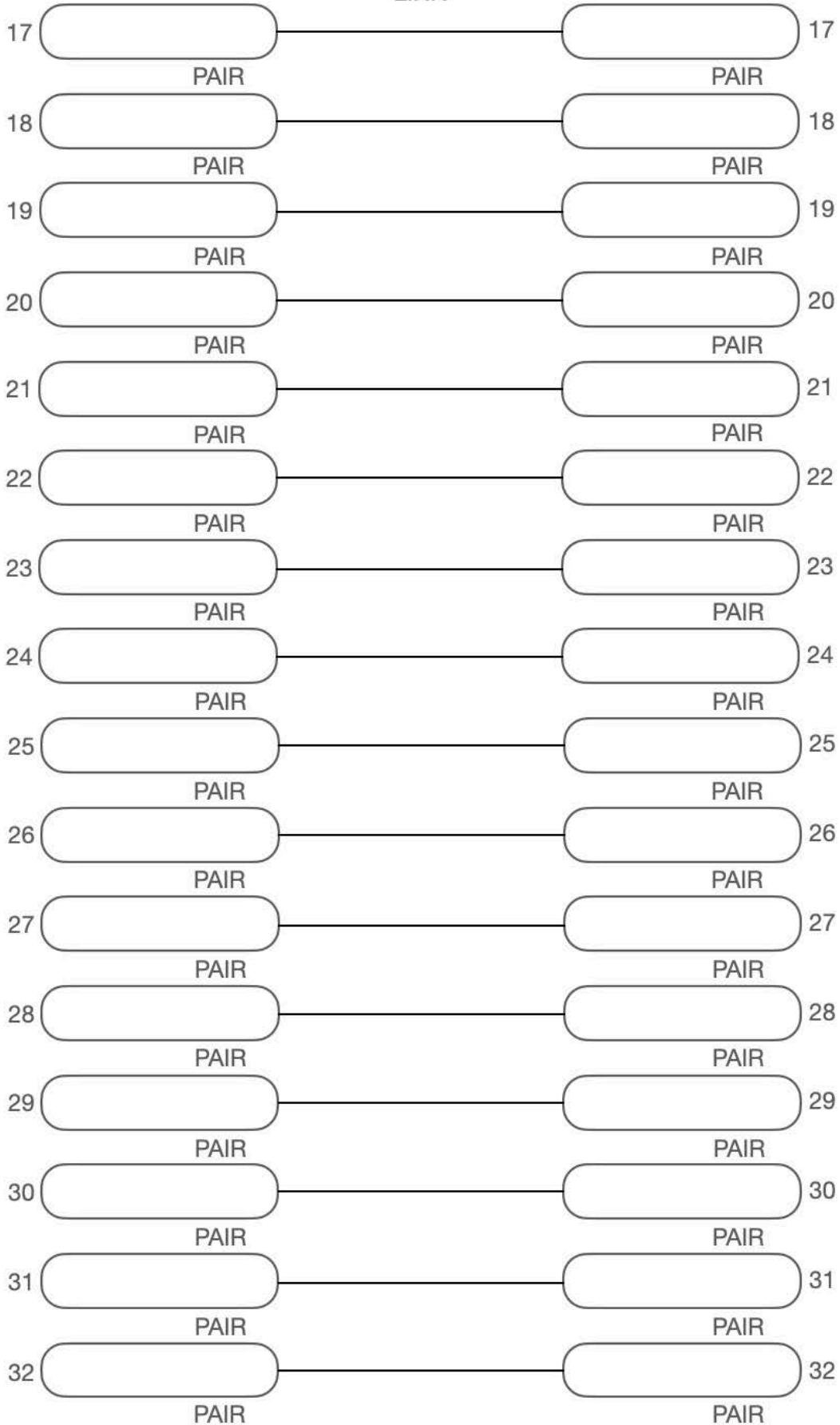


48V

INPUTS

LINK

OUTPUTS

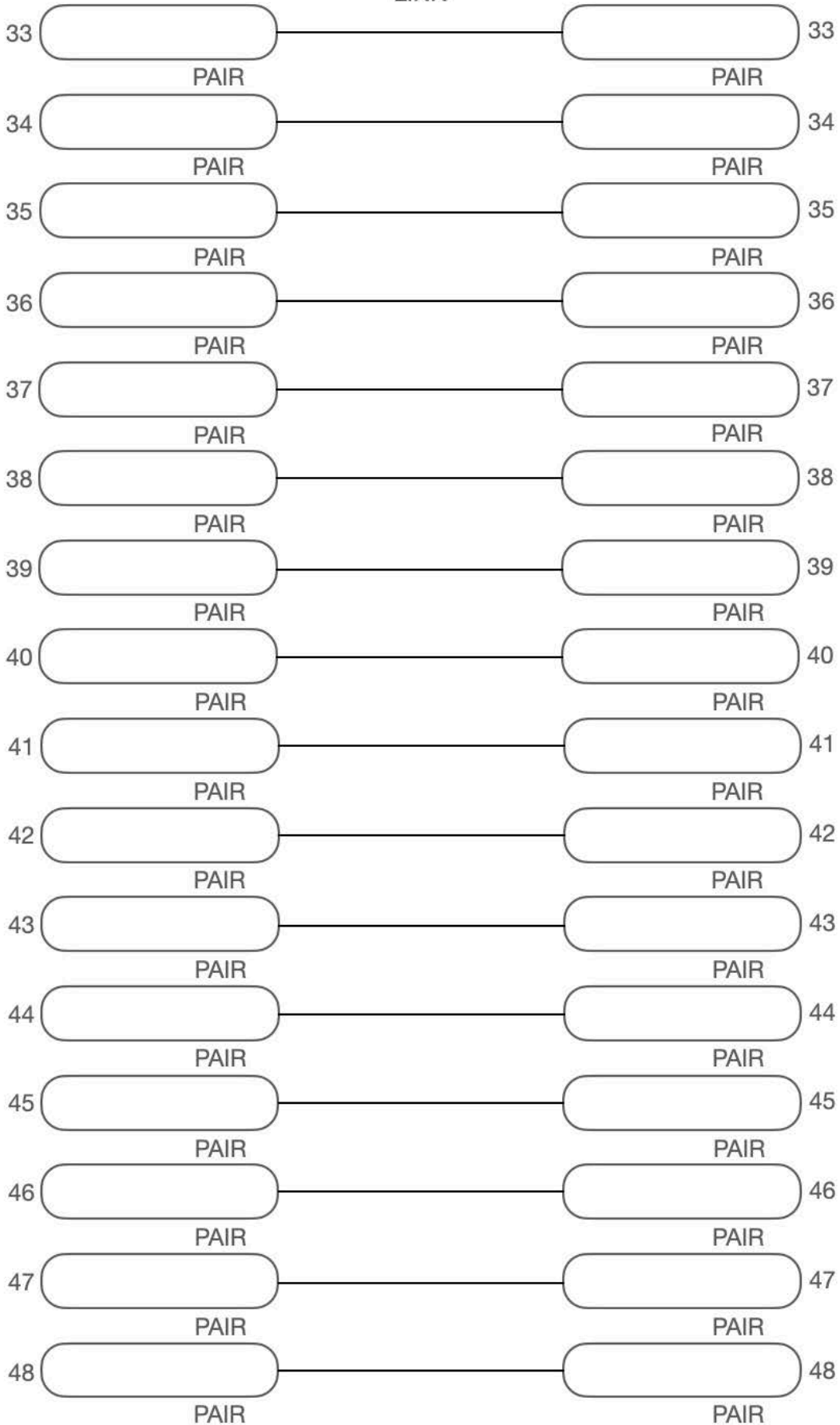


48V

INPUTS

LINK

OUTPUTS



48V

INPUTS

LINK

OUTPUTS

